

**POWER
ARCHITECTURE
COMPUTE-
INTENSIVE
SOLUTIONS**

Technical training is at the heart of the Power Architecture Developer Conference. The technical sessions, panels, and labs will capture a wide range of interests and applications, encouraging the cross-pollination of ideas and the exchange of technical know-how. The presentations have been organized by discipline.

This schedule focuses on the discipline **"Power Architecture Compute-Intensive Solutions"**. Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
10:15 – 11:15 a.m.	Clustering PS3s <i>Terra Soft Solutions</i>
11:15 a.m. – 12:15 p.m.	P6 and Decimal Floating Point <i>IBM</i>
1:45 – 2:45 p.m.	Optimizing Radix-2 FFT Algorithm for PowerPC Architecture <i>Thales Computers</i>
Tuesday	
8:30 – 9:30 a.m.	Implementing the Linux ADMA Interface for Embedded RAID with PowerPC <i>AMCC</i>
9:30 – 10:30 a.m.	POWER6 Reliability and Management <i>IBM</i>
10:45 – 11:45 a.m.	LittleFe: Portable Parallel Computing Platforms for Pedagogy <i>Kean University</i>
1:45 – 2:45 p.m.	Lattice QCD on the Cell Broadband Engine <i>DESY Zeuthen</i>

To register, go to www.power.org/devcon.

**QUESTIONS?
CONTACT US.**

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