

**DIGITAL MEDIA  
BUILT ON POWER  
ARCHITECTURE  
TECHNOLOGY**

Technical training is at the heart of the Power Architecture Developer Conference. The technical sessions, panels, and labs will capture a wide range of interests and applications, encouraging the cross-pollination of ideas and the exchange of technical know-how. The presentations have been organized by discipline.

This schedule focuses on the discipline "**Digital Media Built on Power Architecture Technology**". Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
2:45 – 3:45 p.m.	<b>A Soft Computing Multi-core Solution for Image Restoration</b> <i>Sona College of Technology</i>
Tuesday	
10:45 – 11:45 a.m.	<b>Digital Video Processing with Power Architecture Technology</b> <i>Freescale Semiconductor</i>
1:45 – 2:45 p.m.	<b>Sourcery VSIPL++ for Cell B.E.</b> <i>CodeSourcery, Inc.</i>

To register, go to [www.power.org/devcon](http://www.power.org/devcon).

**QUESTIONS?  
CONTACT US.**

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