

**MULTI-CORE
PARADIGM SHIFT
AND POWER
ARCHITECTURE™
TECHNOLOGY**

Technical training is at the heart of the Power Architecture Developer Conference. The technical sessions, panels, and labs will capture a wide range of interests and applications, encouraging the cross-pollination of ideas and the exchange of technical know-how. The presentations have been organized by discipline.

This schedule focuses on the discipline “**Multi-core Paradigm Shift and Power Architecture™ Technology**”. Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
10:15 – 11:15 a.m.	POWER6 Overview <i>IBM</i>
	Next Generation Multi-GHz Multi-Core Power Architecture CPU <i>AMCC</i>
11:15 a.m. – 12:15 p.m.	Performance Evaluation of SMP Power Architectures <i>EEMBC</i>
	Hybrid Multi-Processing with Embedded PowerPC 405 Cores in FPGAs <i>Xilinx</i>
	Multi-Core Software Design Considerations <i>Freescale Semiconductor</i>
1:45 – 2:45 p.m.	ADL/uADL: A Comprehensive Microprocessor Modeling Framework <i>Freescale Semiconductor</i>
	Using Simulated Hardware to Debug Multi-core Software <i>Virtutech</i>
4:00 – 5:00 p.m.	Parallelism, Power Efficiency, and Programmability: Challenges for Future Architectures <i>University of Texas, Austin</i>
	The Cmpware CMP-DK for the Cell BE <i>Cmpware, Inc.</i>
Tuesday	
8:30 – 9:30 a.m.	Workbench On-Chip Debugging Freescale MPC8641D and other Multi-core Devices <i>Wind River</i>
	Design of 2GHZ High Performance Low Power Dual-core Processor <i>PA Semi</i>
9:30 – 10:30 a.m.	Key Design Challenges and Opportunities for Controlling Power in a Multi-Core Design <i>Freescale Semiconductor</i>
12:45 - 1:45 p.m.	Hybrid Multi-core Debugging Solution on a Power Architecture Embedded Platform <i>Freescale Semiconductor</i>
3:00 – 4:00 p.m.	TCP/IP Acceleration in Cell Broadband Engine Based Platforms <i>Aricent</i>

To register, go to www.power.org/devcon.

**QUESTIONS?
CONTACT US.**

E-Mail: PADCinfo@power.org
Phone +1-512-215-4831
<http://www.power.org/devcon>