

## VIRTUALIZATION – HARDWARE AND SOFTWARE PERSPECTIVES

Technical training is at the heart of the Power Architecture Developer Conference. The technical sessions, panels, and labs will capture a wide range of interests and applications, encouraging the cross-pollination of ideas and the exchange of technical know-how. The presentations have been organized by discipline.

This schedule focuses on the discipline "**VIRTUALIZATION – HARDWARE AND SOFTWARE PERSPECTIVES**." Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
2:45 – 3:45 p.m.	<b>Accelerated Verification of PowerPC Embedded SoCs</b> <i>L3 Communications</i>
Tuesday	
10:45 – 11:45 a.m.	<b>OpenEmbedded</b> <i>OP SIS</i>
	<b>Cell SDK</b> <i>IBM</i>
12:45 – 1:45 p.m.	<b>Using the GNU Toolchain to Build Power Architecture Applications</b> <i>CodeSourcery, Inc.</i>
	<b>Slimline Open Firmware Tutorial/Lab</b> <i>IBM</i>
1:45 – 2:45 p.m.	<b>Migrating Little-Endian to Big-Endian Architectures</b> <i>Freescale Semiconductor</i>
3:00 – 4:00 p.m.	<b>Unleashing the Power with Advanced Compiler Optimizations</b> <i>Green Hills Software</i>
	<b>Hands-On Lab: How Fast Can You Run Linux 2.6 on Freescale PowerQUICC™ Board?</b> <i>Freescale Semiconductor</i>

To register, go to [www.power.org/devcon](http://www.power.org/devcon).

### QUESTIONS? CONTACT US.

E-Mail: [PADCinfo@power.org](mailto:PADCinfo@power.org)  
Phone +1-512-215-4831  
<http://www.power.org/devcon>