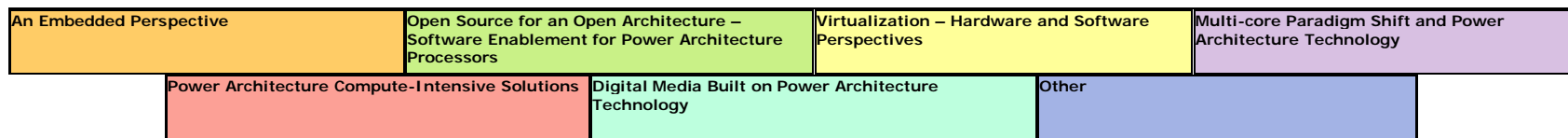


Time	Room 17B	Room 18A	Room 18B	Room 18C	Room 18D	Room 19A	Lab Room 19B
7:30 – 8:30 a.m.	REGISTRATION AND CONTINENTAL BREAKFAST						
8:30 – 10:00	Main Tent Welcome/Introduction of Keynote Speakers– Mike Paczan (Chairman Power.org Technical Committee) <ul style="list-style-type: none"> • Keynote: Brian Wilkie (AMCC) • Keynote: Lynelle McKay (Freescale Semiconductor) • Keynote: Jim Kahle (IBM) 						
10:00 – 10:15	BREAK						
10:15 – 11:15	MC02 POWER6 Overview <i>IBM</i>	EP01 WiMAX Based Solution for Infrastructure and CPE <i>Freescale Semiconductor</i>	MC01 Next Generation Multi-GHz Multi-core Power Architecture CPU <i>AMCC</i>	CI01 Clustering PS3s <i>Terra Soft Solutions</i>	EP16 New Developments in Real-Time Embedded Linux <i>MontaVista</i>	OT06 Next Generation Power Architecture _Panel_	EP15 Hands-on Workshop: Efficient Programming with the QUICC Engine Utility Tool for Freescale PowerQUICC™ Processors <i>Freescale Semiconductor</i>
11:15 – 12:15 p.m.	EP02 Integrating Power Architecture Core IP in SoCs <i>IPextreme</i>	EP14 Virtual Integration of PowerQUICC Firmware and Peripherals <i>Mentor Graphics</i>	MC05 Performance Evaluation of SMP Power Architectures <i>EEMBC</i>	CI06 P6 and Decimal Floating Point <i>IBM</i>	MC03 Hybrid Multi-Processing with Embedded PowerPC 405 Cores in FPGAs <i>Xilinx</i>		
12:15 – 1:45	LUNCH in the Landscape						
1:45 – 2:45	EP03 On-Chip Bus Architectures for Power Architecture <i>Denali Software, Inc.</i>	CI02 Optimizing Radix-2 FFT Algorithm for PowerPC Architecture <i>Thales Computers</i>	MC06 ADL/uADL: A Comprehensive Microprocessor Modeling Framework <i>Freescale Semiconductor</i>	EP04 PowerPC Embedded MMU and Cache Management <i>Technionics, Inc.</i>	MC07 Using Simulated Hardware to Debug Multi-core Software <i>Virtutech</i>	OT01 Power ISA Tutorial	
2:45 – 3:45	DM02 A Soft Computing Multi-core Solution for Image Restoration <i>Sona College of Technology</i>	EP05 New Verification Methods for Power Based SOC Designs <i>EVE-USA</i>	VR01 High Assurance Security for Power-based Embedded Systems <i>Green Hills</i>	EP06 Titan - An Ultra High-perf., Power and Area-efficient Core <i>Intrinsity, Inc.</i>	SW02 Accelerated Verification of PowerPC Embedded SoCs <i>L3 Communications</i>		
3:45 – 4:00	BREAK						
4:00 – 5:00	EP07 Industrial Network Protocols on Communications Processors <i>Freescale Semiconductor</i>	VR02 IBM Power6 Partition Mobility <i>IBM</i>	EP08 Multi-protocol Mapping with PPC405EX Processor <i>AMCC</i>	VR03 Virtualization Features of PWRficient 1682M <i>P.A. Semi</i>	MC08 Parallelism, Power Efficiency, and Programmability: Challenges for Future Architectures <i>University of Texas, Austin</i>	Power Architecture Feedback Session	MC15 The Cmpware CMP-DK for the Cell BE <i>Cmpware, Inc.</i>
5:00 – 7:00	POWER MIXER in the Landscape						



Time	Room 17B	Room 18A	Room 18B	Room 18C	Room 18D	Room 19A	Lab Room 19B
7:30 – 8:30 a.m.	BREAKFAST						
8:30 – 9:30	MC16 Workbench On-Chip Debugging Freescale MPC8641D and Other Multi-core Devices <i>Wind River</i>	VR04 Virtualization on Power <i>IBM</i>	CI05 Implementing the Linux ADMA Interface for Embedded RAID with PowerPC <i>AMCC</i>	MC09 Design of 2GHZ High Performance Low Power Dual-core Processor <i>P.A. Semi</i>	EP17 Rapid Assembly of Power-Based SoCs <i>Synopsys</i>	OT02 Overview of Current Power Processor Standard Product Offerings - Panel	SW01 Hands-on Workshop: How Fast Can You Run Linux 2.6 on Freescale PowerQUICC™ Board? <i>Freescale Semiconductor</i>
9:30 – 10:30	MC10 Multi-Core Design: Key Challenges and Opportunities <i>Freescale Semiconductor</i>	EP09 Digital Processing with Power Architecture in Automotive <i>Freescale Semiconductor</i>	OT07 Environmental Impact of Chip Design Practices <i>Cadence</i>	OT03 POWER6 Reliability and Management <i>IBM</i>	MC17 Automotive Qualified Multi-core Microprocessor for Telematics and Industrial Control Systems <i>Freescale Semiconductor</i>		
10:30 – 10:45	BREAK						
10:45 – 11:45	DM01 Digital Video Processing with Power Architecture Technology <i>Freescale Semiconductor</i>	SW03 OpenEmbedded <i>Digital OPSIS</i>	EP10 Chaining On-chip Accelerators in Power Architecture <i>Mindtree Consulting Ltd. Bangalore, India</i>	CI03 LittleFe: Portable Parallel Computing Platforms for Pedagogy <i>Kean University</i>	SW07 Cell SDK Tutorial <i>IBM</i>	OT04 Power.org Technical Subcommittee Overview and Audience Feedback Panel	SW01 Hands-on Workshop: How Fast Can You Run Linux 2.6 on Freescale PowerQUICC™ Board? – contin. <i>Freescale Semiconductor</i>
11:45 – 12:45 p.m. LUNCH (BOFS)	LUNCH BOFS - Software Development for Power Architecture	LUNCH BOFS - SOC Design Tools (EDA Tool Companies)	LUNCH BOFS - Power Efficient Design (Power and Thermal Considerations)	LUNCH BOFS - Performance Optimization for Power Architecture	LUNCH BOFS – Multi-core		
12:45 – 1:45	OT05 An Automatic System for PowerPC Architecture Compliance Validation <i>IBM</i>	MC12 Hybrid Multi-core Debugging Solution on a Power Architecture Embedded Platform <i>Freescale Semiconductor</i>	VR05 Xen Hypervisor for Cell Broadband Engine <i>Samsung</i>	SW04 Using the GNU Toolchain to Build Power Architecture Applications <i>CodeSourcery, Inc.</i>	SW08 Slimline Open Firmware Tutorial <i>IBM</i>	MC11 Multi-core Enablement - Panel	
1:45 – 2:45	SW05 Migrating Little-Endian to Big-Endian Architectures <i>Freescale Semiconductor</i>	CI04 Lattice QCD on the Cell Broadband Engine <i>DESY Zeuthen</i>	EP11 System Design with the P.A. Semi PA6T-1682M <i>P.A. Semi</i>	DM03 Sourcery VSIPL++ for Cell B.E. <i>CodeSourcery, Inc.</i>			
2:45 – 3:00	BREAK						
3:00 – 4:00		EP12 Integrated Multi-Application Safety Critical and Secure Systems <i>GE Aviation</i>	MC13 An Approach to Multi-core SoC Design <i>Freescale Semiconductor</i>	SW06 Unleashing the Power with Advanced Compiler Optimizations <i>Green Hills Software</i>	EP13 High Performance Secure Embedded Computing for the Cell Broadband Processor <i>IBM</i>	MC04 Freescale Simulation Technology <i>Freescale Semiconductor</i>	
4:00 – 5:00	Closing Keynote Speaker Jim Bell and Raffle						

An Embedded Perspective	Open Source for an Open Architecture – Software Enablement for Power Architecture Processors	Virtualization – Hardware and Software Perspectives	Multi-core Paradigm Shift and Power Architecture Technology
Power Architecture Compute-Intensive Solutions	Digital Media Built on Power Architecture Technology	Other	