

AEROSPACE &
DEFENSE

The Power Architecture Developer Conference offers 65+ hours of sessions, labs and panels covering a wide array of interests and applications. The following sessions are particularly applicable to your market segment. Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
10:15 - 11:15 a.m.	POWER6 Overview, <i>IBM</i>
	Next Generation Multi-GHz Multi-Core Power Architecture CPU, <i>AMCC</i>
	Clustering PS3s, <i>Terra Soft Solutions</i>
11:15 a.m. - 12:15 p.m.	P6 and Decimal Floating Point, <i>IBM</i>
1:45 - 2:45 p.m.	Optimizing Radix-2 FFT Algorithm for PowerPC Architecture, <i>Thales Computers</i>
2:45 - 3:45 p.m.	A Soft Computing Multi-core Solution for Image Restoration, <i>Sona College of Technology</i>
	High Assurance Security for Power-based Embedded Systems, <i>Green Hills</i>
	Titan - An Ultra High-Performance., Power and Area-efficient Core, <i>Intrinsity, Inc</i>
4:00 - 5:00 p.m.	IBM Power6 Partition Mobility, <i>IBM</i>
Tuesday	
8:30 - 9:30 a.m.	Virtualization on Power, <i>IBM</i>
9:30 - 10:30 a.m.	POWER6 Reliability and Management, <i>IBM</i>
10:45 - 11:45 a.m.	Digital Video Processing with Power Architecture Technology <i>Freescale Semiconductor</i>
	LittleFe: Portable Parallel Computing Platforms for Pedagogy, <i>Kean University</i>
1:45 - 2:45 p.m.	Lattice QCD on the Cell Broadband Engine, <i>DESY Zeuthen</i>
	System Design with the P.A. Semi PA6T-1682M, <i>P.A. Semi</i>
	Sourcery VSIPL++ for Cell B.E., <i>CodeSourcery, Inc.</i>
3:00 - 4:00 p.m.	Integrated Multi-Application Safety Critical and Secure Systems, <i>GE Aviation</i>
	High Performance Secure Embedded Computing for the Cell Broadband Processor, <i>IBM</i>

To register, go to www.power.org/devcon.

QUESTIONS?
CONTACT US.

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