

AUTOMOTIVE

The Power Architecture Developer Conference offers 65+ hours of sessions, labs and panels covering a wide array of interests and applications. The following sessions are particularly applicable to your market segment. Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
11:15 a.m. - 12:15 p.m.	Integrating Power Architecture Core IP in SoCs , <i>IPextreme</i>
1:45 - 2:45 p.m.	PowerPC Embedded MMU and Cache Management , <i>Technonics, Inc</i>
4:00 - 5:00 p.m.	Virtualization Features of PWRficient 1682M , <i>P.A. Semi</i>
Tuesday	
8:30 - 9:30 a.m.	Design of 2GHZ High Performance Low Power Dual-core Processor , <i>P.A. Semi</i>
9:30 - 10:30 a.m.	Digital Processing with Power Architecture in Automotive , <i>Freescale Semiconductor</i>
10:45 - 11:45 a.m.	OpenEmbedded , <i>OP SIS</i>
12:45 - 1:45 p.m.	An Automatic System for PowerPC Architecture Compliance Validation , <i>IBM</i>
1:45 - 2:45 p.m.	System Design with the P.A. Semi PA6T-1682M , <i>P.A. Semi</i>
3:00 - 4:00 p.m.	Integrated Multi-Application Safety Critical and Secure Systems , <i>GE Aviation</i>

To register, go to www.power.org/devcon.

**QUESTIONS?
CONTACT US.**

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