

The Power Architecture Developer Conference offers 65+ hours of sessions, labs and panels covering a wide array of interests and applications. The following sessions are particularly applicable to your market segment. Additional topics will be covered that may be relevant to your business. To see the full schedule, go to <http://www.power.org/devcon/07/schedule/>.

Monday	Session
10:15 – 11:15 a.m.	POWER6 Overview , <i>IBM</i>
	WiMAX Based Solution for Infrastructure and CPE , <i>Freescale Semiconductor</i>
	Next Generation Multi-GHz Multi-Core Power Architecture CPU , <i>AMCC</i>
11:15 a.m. - 12:15 p.m.	Integrating Power Architecture Core IP in SoCs , <i>IPextreme</i>
	Virtual Integration of PowerQUICC Firmware and Peripherals , <i>Mentor Graphics</i>
	P6 and Decimal Floating Point , <i>IBM</i>
	Hybrid Multi-Processing with Embedded PowerPC 405 Cores in FPGAs , <i>Xilinx</i>
1:45 - 2:45 p.m.	PowerPC Embedded MMU and Cache Management , <i>Technonics, Inc</i>
	Using Simulated Hardware to Debug Multi-core Software , <i>Virtutech</i>
2:45 – 3:45 p.m.	High Assurance Security for Power-based Embedded Systems , <i>Green Hills</i>
	Titan - An Ultra High-perf., Power and Area-efficient Core , <i>Intrinsity, Inc</i>
4:00 - 5:00 p.m.	Multi-protocol Mapping with PPC405EX Processor , <i>AMCC</i>
	Virtualization Features of PWRficient 1682M , <i>P.A. Semi</i>
Tuesday	
8:30 - 9:30 a.m.	Virtualization on Power , <i>IBM</i>
	Implementing the Linux ADMA Interface for Embedded RAID with PowerPC , <i>AMCC</i>
	Design of 2GHZ High Performance Low Power Dual-core Processor , <i>P.A. Semi</i>
9:30 - 10:30 a.m.	POWER6 Reliability and Management , <i>IBM</i>
10:45 - 11:45 a.m.	OpenEmbedded , <i>OPSSIS</i>
	Chaining On-chip Accelerators in Power Architecture , <i>Mindtree Consulting Ltd. Bangalore, India</i>
12:45 - 1:45 p.m.	Xen Hypervisor for Cell Broadband Engine , <i>Samsung</i>
1:45 - 2:45 p.m.	Migrating Little-Endian to Big-Endian Architectures , <i>Freescale Semiconductor</i>
	System Design with the P.A. Semi PA6T-1682M , <i>P.A. Semi</i>
3:00 – 4:00 p.m.	TCP/IP Acceleration in Cell Broadband Engine Based Platforms , <i>Aricent</i>
	An Approach to Multi-Core SoC Design , <i>Freescale Semiconductor</i>

To register, go to www.power.org/devcon.

NETWORKING & COMMUNICATIONS

QUESTIONS? CONTACT US.

E-Mail: PADCInfo@power.org
Phone +1-512-215-4831
<http://www.power.org/devcon>