Technical Committee Newsletter

2Q2007

In This Issue

Greetings from the Chair

Team,

I want to take this opportunity to recognize the recent leadership contributions of Nina Wilner, Warren Savage, and Kimberly Fountain. Nina orchestrated a successful full-day Software Summit meeting on 19 April. Warren and Kimberly have re-established our Disruptive Play Work Group as the place for our most creative minds to form new ideas that add new vitality and vision to Power.org.

Our next quarterly Technical Committee face to face is scheduled for 12 June at the IBM Innovation Center, San Mateo, California. At this meeting, our plan is to review and discuss the IDC Power Architecture Market Intelligence Report jointly with the Marketing Committee. With this report as background, we will then begin our annual Power.org technical objectives and strategic planning for 2008. Contributing to this planning effort will be a detailed discussion with our Disruptive Play Work Group led by Warren Savage and a discussion on the role of "solution plays" in our ecosystem development strategy led by me. In preparation, please think about the areas that Power.org should consider for our collaborative technical efforts in 2008 that would contribute most to Power Architecture ecosystem development. Our intent is to complete this annual planning in the 3rd quarter so that any resulting technical subcommittees can be formed and launched in 4th quarter.

I want to make each of you aware that Ed Angelovich and a small team have been discussing holding a "Power.org Communication Day" in Japan, China, and Taiwan during the period of 16-27 July. The goal would be to recruit new member companies and communicate with existing regional members. Direct participation by regional members in our technical committee and subcommittees meetings has always been a difficult challenge; consequently, we want to use these communication "days" to allow us to establish direct face to face communication with our regional members. Planned for these meetings are detailed reviews of our Power.org marketing and technical initiatives along with three to four education and member company presentations to round out the agenda. These communication meetings will not substitute for, or duplicate, the Developer Conference. Power.org members who would like to participate, please contact Ed Angelovich [epa@us.ibm.com].

Opportunities exist for members to contribute to industry magazine articles that help us communicate our Power ecosystem work. If you are interested in writing an article or assisting an industry journalist with developing an article please contact Bonnie Quintanilla at 818-681-5777, bonnie@corridorcomms.com or press@power.org.

Sincerely,

Michael Paczan
Chairman, Power.org Technical Committee
tech-chair@power.org

Urgent News & Member Announcements

- **June Power.org face to face meetings**

  IBM will host the next face to face meetings at the IBM Innovation Center, San Mateo, California. The Marketing and Technical Committees will meet 12 June and the Board of Directors will meet 13 June.

  Register by May 28 at http://www.power.org/news/events/jun07mtg/

- **Dream It, Design It, Build It - Integrating Power Architecture™ e200 Cores**

  Freescale's e200 Power Architecture™ core family is now available through independent licensing expert IPextreme. This webcast, sponsored by Freescale and IPextreme, both Power.org members, covers an overview of the e200 core family applications and technology, how to efficiently integrate e200 cores into embedded applications, and information about the licensing process and support. Get a jump start on planning your next system-on-chip (SoC) design by attending this webcast on 24 May at 10:00AM PDT

  Register today for this informative session.
  http://seminar2.techonline.com/registration/distrib.cgi?s=1073&d=641

- **Call for Nominations – Power.org Marketing, Technical, Operations Committee Chairs**

  The Marketing, Technical, and Operations Committees Chair positions are up for re-election in June. This is an opportunity to influence the direction of Power.org and help the organization achieve its goals. Nominees must be committed to investing the time needed to properly lead a committee and have the support of their management to perform the role.

  If you would like to nominate yourself, or someone else, for any of the committee chair positions, please return the following information to joan.woolery@ieee-isto.org by June 1:

  - Brief bio for the nominee
  - Brief statement on why the nominee wishes to chair that committee

- **Power Architecture™ Developer Conference**

  The excitement is building and Power.org Technical and Marketing Committees are well into the execution phase of preparing the inaugural Power Architecture™ Developer Conference. The conference is going to be held on 24-25 September at the Austin Convention Center in Austin, Texas, the city also known as the birthplace of Power Architecture technology. With local presence of IBM, Freescale, and AMCC, as well as support from all Power.org members, this conference aims to be dedicated to the hardware and software developers who are building innovative solutions using Power Architecture technology.

  The call for papers received a good response and the technical review subcommittee is currently working its way through more than seventy one submissions that contain very compelling and educational content. We continue to allow for a few late submissions, especially in the discipline areas of "Globally Green" and "Virtualization", and will consider those on a case by case basis. We are also planning some hands-on labs and a showcase of solutions built on Power Architecture technology. The energy and promise of a truly collaborative technical organization such as ours is set to unleash a new way of serving the most important users of the Power Architecture platform – the developers. If you are a hardware or software developer working on solutions based on Power Architecture technology, we would like to hear from you and do send your suggestions. We look forward to your attendance and active participation.

  Early bird pricing ends 15 July. Register today at http://www.power.org/devcon/07.

- **Power Architecture™ Market Intelligence Report**

  Power.org, in conjunction with Freescale and IBM, has commissioned IDC to conduct custom market research on Power Architecture technology. IDC's report will provide market share information by processor architecture in dozens of product segments in the automotive, communications, computing, consumer and industrial markets. The report will also provide a competitive analysis, a qualitative market analysis and recommendations for growing Power's market opportunities. The report should be completed in June and will be distributed to Power.org's corporate members at the Founder, Sponsor and Participant levels, each of whom will be granted a company-wide license. For more information, please contact Tein Atkerson at tein@power.org.

- **New Power.org Corporate Members**

  Welcome new members Lauterbach, LynuxWorks, Tundra, VaST Systems and XGI to the Power.org family.

- **Free Debug Monitor for Freescale's MPC5200**

  Developer member Dimiter Popoff, of Transgalactic Instruments, encourages you to check out his debug monitor (free for development purposes) for Freescale's MPC5200 at http://tgi-sci.com/tgi/download/m52.htm.

Technical Updates

Technical Committee

Work continues on our core 2007 technical initiatives with progress summaries provided by each Technical Subcommittee and Work Group lead in the following sections. We continue to work recruiting of new members in the software area with particular emphasis near-term on those companies who we think could contribute to the proposed Embedded Software Framework initiative.

Based on recent discussions, there is a need to accelerate the pace of work efforts around virtualization and hypervisor architecture. This work will involve both the PAAC and other Technical Subcommittees. If you want to contribute to this area, please communicate your interest to Wolfram Sauer [wsauer@us.ibm.com] and Nina Wilner [new_nina@us.ibm.com]

I would like to thank those of you who have been contributing to the Technical Committee. Your participation and contributions are deeply appreciated.

Sincerely,

Michael Paczan
Chairman, Power.org Technical Committee

Objective: Establish and maintain the overall technical vision and goals for Power.org. Responsible for forming and supervising Technical Subcommittees (TSC) that develop reports, specifications and reference designs.

Chairman: Michael Paczan (IBM) [tech-chair@power.org]

Members: AMCC, Cadence, Chartered Semiconductor, Denali, Ericsson, Freescale, IBM, Mentor Graphics, Rapport, Silicon Application, Sony, Synopsys, Thales, Venture Corp, Virtutech, Wind River, Wistron, Xilinx

Meetings: Monthly conference calls and quarterly face to face meetings.

Home page: http://www.power.org/apps/org/workgroup/tech/

Join group: http://www.power.org/apps/org/workgroup/tech/join.php

Group Status:

- Disruptive Play Work Group restarted and three “brainstorming” meetings held to generate new ideas.
- Discussions are underway regarding whether a mutually beneficial linkage should be created between Power.org and the Multicore Association.
- Ed Angelovich recently reviewed with the TC a team developed proposal that would enable Developer level participation in Technical Subcommittees. The proposal must be reviewed and approved by the Board before it can be implemented. Ed is looking proposal feedback from TC team members. [http://www.power.org/apps/org/workgroup/tech/download.php/1454] If implemented, the proposal is expected to give some of our technical subcommittee and work group leads the ability to tap into new resources to contribute towards their team objectives.

Events:

- Technical Committee face to face meeting – 12 June, San Mateo, CA.
- Power.org Communication Day – 16-27 July; Shanghai, Taipei, and Tokyo.
- Developer Conference – 24-25 September, Austin, Texas.

Power Architecture Advisory Council

Objective: Discuss ISA contributions to the Power Architecture ISA and vote on whether to make recommendations to IBM regarding such ISA contributions.

Chairman: Wolfram Sauer (IBM) [paac-chair@power.org]

Members: Freescale, IBM

Home page: http://www.power.org/apps/org/workgroup/paac/

Specifications:

- **Power ISA Version 2.04** (.pdf 7MB) [http://www.power.org/resources/downloads/PowerISA_204-FINAL.Public.pdf]
- **Power ISA Version 2.03** (.pdf 7MB) [http://www.power.org/news/articles/new_brand/#isa]

Group Status:

- Power ISA Version 2.04 was approved by the PAAC and has been presented to the Power.org Technical Committee. It is now available to all Power.org members (see link above).

Key Deliverables:

- 4Q07 - Power ISA Version 2.05.

Bus Architecture TSC

The Bus Architecture TSC continues to meet and discuss the low- and mid-performance buses in preparation for generating a SoC Bus Hierarchy and Strategy Report. It is still apparent that the CoreConnect and AMBA bus families must both be supported in order to meet the various requirements of embedded Power Architecture SoCs. The members will investigate how to best incorporate both CoreConnect and AMBA cores into a single SoC.

It has become clear that it is sometimes difficult to place a bus into a single performance category. For example, AXI was originally classified as a high-performance bus due to aggregate bandwidth supported, as well as several advanced bus features, such as request pipelining and out-of-order read data. However, since some member companies have used AXI as a bus attached through a bridge to the CPU bus, and since there is no native attachment of any 4xx PowerPC CPU to AXI, it will also be evaluated as a mid-performance bus.

We will enhance the PLB4 Interoperability Specification (PLB4 IOS) in content and visibility. It is important for developers of new PLB4 cores to understand that strictly adhering to the PLB4 IOS is the best way to ensure compatibility with existing PLB4 cores.

The biggest challenge for 2007 is to adopt or specify a high-performance bus that supports Power ISA compatible processors and accelerators. I am excited about discussing high-performance bus attributes with our members, and look forward to evaluating high-performance bus candidates that offer the best support of Power Architecture.

Sincerely,

Jason Hopp
Chairman, Bus Architecture TSC

Objective: Define a hierarchy of bus architectures and support structures to standardize the bus interconnect in Power based SoCs, enabling rapid reuse, lower development costs, and increased compatibility.

Chairman: Jason Hopp (IBM) [barch-chair@power.org]

Members: AMCC, Cadence, Denali, Ericsson, Freescale, HCL Technologies, IBM, Mercury Computer, P.A. Semi, Synopsys

Meetings: Wednesdays 11:00-12:00 PM EST

Home page: http://www.power.org/apps/org/workgroup/barch/

Join group: http://www.power.org/apps/org/workgroup/barch/join.php

Group Status:

- In the process of revisiting the low-performance and mid-performance levels to generate a SoC Bus Hierarchy and Strategy Report.
- Beginning work on the high-performance bus strategy

Key Deliverables:

- Vision and direction white paper — Complete
- Low-performance bus report — Complete
- Mid-performance bus report — Complete
- Updates to the mid-performance bus IOS
- SoC Bus Hierarchy and Strategy Report
- High-performance bus report

Common Debug Interface TSC

The Common Debug Interface TSC is born.

Thanks to the diligent work from members of AMCC, Freescale, IBM, Lauterbach, Mentor Graphics, Virtutech and Wind River, the scope document was approved by the Technical Committee and the Board of Directors. With the approval of the document the TSC was officially formed in April. The scope document can be viewed or downloaded from the Common Debug Interface TSC home page.

As laid out in the scope document, the TSC will focus its standardization work in three major areas: 1) The Physical and Logical Connections between the debug probe and the target. 2) Target Capabilities to facilitate a better debug environment. 3) Application Programming Interface between the debug host, the probe and the target.

To efficiently tackle these three areas, three separate teams were formed within the TSC. The Physical team is led by Arthur LaVita of Wind River. The Target team is led by Dan Helm of Freescale. The API team is led by Erich Styger also from Freescale. Currently, each team meets once a week. If you are interested in joining any of these teams, or simply want to know what we are doing, please contact me.

Please also visit the document area of our home page to read more details of what we have accomplished so far.

Additional summary TSC status is provided below.

Sincerely,

Chris Ng
Chairman, Common Debug Interface TSC

Objective: Identify and standardize aspects of common debug environment, interface and methodology (both software and hardware) for Power™ Architecture

Chairman: Chris Ng (IBM) [debug-chair@power.org]

Members: AMCC, Freescale, IBM, Lauterbach, Mentor Graphics, Virtutech, Wind River

Meetings: TSC meets every other Thursday, 12-1PM EST

Home page: http://www.power.org/apps/org/workgroup/debug/

Join group: http://www.power.org/apps/org/workgroup/debug/join.php

Group Status:

- Power.org BoD approved scope proposal
- TSC is formed with three sub teams:
  Physical team
  - Evaluating industry standard interfaces to adopt
  - Defining connector(s), pinouts, cable length

  Target capabilities
  - Defining high speed trace support
  - Bandwidth needed
  - Flow control

  API
  - Defining a set of services between debug host, probe and target

Platform Architecture TSC

The Platform Architecture TSC remains active on two main fronts: developing the Embedded Power Architecture Platform Requirements (ePAPR) specification, and recruitment.

First sections of the Embedded PAPR are coming to life, and are beginning to be reviewed, while other sections continue to be developed. Freescale continues as the main contributor, but the committee is on the verge of starting a few more authors.

Recruitment continues and is mostly focused on software developers and software companies, as software developers and companies are one of the main customers of ePAPR! Wind River plans to name someone to join the committee and effort continues to enlist Linux kernel developers, either from member companies, or as developer members.

HCL America, P.A. Semi, and Wind River have recently joined the TSC roster – Welcome!!

Sincerely,

Dave Willoughby
Chairman, Bus Architecture TSC

Objective: Define and publish base platform arch standards to facilitate development of compliant components and products

Chairman: Dave Willoughby (IBM) [parch-chair@power.org]

Members: AMCC, Cadence, Ericsson, Freescale, HCL America, IBM, IPextreme, P.A. Semi, Thales, Wind River, Wistron

Meetings: Embedded PAPR development bi-weekly conference calls, every other Wednesday 11:30-12:30PM EST. Meetings to discuss server PAPR are scheduled as needed.

Home page: http://www.power.org/apps/org/workgroup/parch/

Join group: http://www.power.org/apps/org/workgroup/parch/join.php

Specifications: PAPR Version 2.0 [http://www.power.org/members/developers/specs/PAPR]

Group Status:

- Scope of embedded PAPR version 1.0 defined. Initial outline of ePAPR specification completed. Developing various boot architecture topics, such as device tree characteristics, with Freescale providing technical leadership.

Key Deliverables:

- 2Q07 - Embedded PAPR draft version 1.0 approved by TSC
- 4Q07 - Embedded PAPR approved and published

Key Next Steps:

- Continue developing ePAPR specification topics

Reference Platform Design TSC

Objective: Produce Reference designs that implement HW/FW Standards for various classes of Power Architecture Platforms to lower overall cost of products and offerings for members of Power.org and PowerPC Ecosystem.

Scope: Reference designs that incorporate various PPC licensees' micros and will become example implementation of classes of PAPR compliant platforms such as Servers, Workstations, Clients, Laptops, High End Embedded, HPC.

Chairman: Kaveh Massoudian (IBM) [hvarch-chair@power.org]

Members: Cadence, Freescale, IBM, Mercury Computer, Silicon Application Corporation, Terra Soft, Thales, Wistron

Meetings: Meets bi-weekly with the Platform Architecture TSC, every other Wednesday 11-12 EST.

Home page: http://www.power.org/apps/org/workgroup/hvarch/

Join group: http://www.power.org/apps/org/workgroup/hvarch/join.php

Group Status:

- Released the first Power.org 970MP based reference design
- Released the Open System Stack in support of the 970MP reference design. LINUX and XEN are available from their respective open community source trees. SLOF changes were released into Power.org.
- Shifting our focus to the embedded space.

Key Deliverables:

- Board schematics
- Board support package made of system firmware and software based on open source initiatives SLOF, XEN, LINUX.

Key Next Steps:

- Re-evaluating whether we proceed with the refresh of the I/O subsystem for the 970MP reference design to feature PCI-e Gen2.
- Work with Freescale, AMCC, and P.A. Semi to make available their reference designs.
- Evaluating other proposals from member companies.

System on Chip Design Hierarchy TSC

Objective: Adopt and/or create specifications and standards to enable creation of a world-class SoC design eco-system (tools, methodology, and design enablers) which positions the Power architecture at the industry forefront and offers designers the easiest path to a successful microprocessor based System-on-a-Chip design.

Chairman: Magdy Abadir (Freescale) [soc-chair@power.org]

Members: Cadence, Chartered, Ericsson, Freescale, HCL Technologies, IBM, IPextreme, Mentor Graphics, Synopsys

Meetings: As needed

Home page: http://www.power.org/apps/org/workgroup/soc/

Join group: http://www.power.org/apps/org/workgroup/soc/join.php

Group Status:

- Prepared a SoC methodology gap analysis report targeting areas of highest priority including verification, ESL, tradeoff analysis tools, and models.
- Surveyed Power-based SoC design teams to collect their input on what would lower the barriers to adoption of Power cores in SoCs and improve the efficiency of their SoC development around Power Architecture cores.
- Investigated the SPIRIT IP-XACT standard
- Conducted a preliminary investigation of the issues around the availability of models including functional, cycle-accurate, cycle-approximate models, performance/power models. From an architect exploration and implementation point of view as well as from the application software developer point of view. What is needed, what are the barriers, can power.org agree on common formats.
- Prepared recommendation for future directions

Key Next Steps:

- Recruit a team to fully investigate the issues around the availability of models including functional, cycle-accurate, cycle-approximate models, performance/power models. From an architect point of view as well as from the application software developer point of view. What is needed? What are the barriers? Can we get vendors and core providers to put together a feasible solution?
- Potentially, propose the formation of a new working group to focus on models.

Recommendations:

- Endorse SPIRIT IP-XACT to facilitate Power-based SoC creation
- Ensure availability of all Power busses protocol specification in IP-XACT
- Avoid developing/promoting Power-specific tool/methodology solutions. Instead, leverage existing solutions and industry-wide efforts.
- Explore the establishment of a new working group that will focus on models
- Accelerate the efforts of the Bus Architecture TSC – key enabler for IP development and SoC design.
- Continue the efforts in the Common Debug Interface TSC
- Conclude the activities of the SoC Design Hierarchy TSC

Disruptive Play TWG

The Disruptive Play TWG needs your company's most creative and energetic minds. If you know people like this that would like to get involved with developing ground-breaking, paradigm shattering ideas to advance Power Architecture— this is the place.

Last year, the Disruptive Play TWG engaged the Power.org membership to put forth new disruptive ideas around Power Architecture that could be taken to the venture community for funding and promoted by the Power community. Several ideas were successfully incubated there and are now being developed. This year, the Technical Committee resolved it would like to see how the role of the Disruptive Play TWG could be expanded to show even more value to the Power.org membership.

At the March face-face meeting held in Austin, Texas, Warren Savage (IPextreme) and Kimberly Fountain (IBM) agreed to hold a series of brainstorming sessions during Q2 to collect ideas from the membership for the charter of the Disruptive Play TWG and how best that TWG can harness the creative energy of the membership to drive disruptive plays into reality.

The group has been very productive, meeting three times since early April with broad participation from semiconductor, software, IP, and EDA member companies. More than thirty ideas have been floated ("Storming") by the team which now heads into a phase where the ideas are consolidated into candidates for further work by the group and refinement of the group charter. Subsequent meetings are being held throughout May for "Norming" and "Forming" in preparation for a proposal to be provided to the Power.org Board of Directors in June.

Additional summary TWG status is provided below.

Sincerely,

Warren Savage
Acting Co-Chair, Disruptive Play TWG

Objective: Provide a collaborative environment in Power.org to develop disruptive plays around Power Architecture.

Chairman: Acting co-chairs Warren Savage (IPextreme) and Kimberly Fountain (IBM) [dp-chair@power.org]

Members: Freescale, IBM, IPextreme, Mercury Computer, Rapport, Terra Soft, VaST

Meetings: No regular schedule has been established

Home page: http://www.power.org/apps/org/workgroup/dp/

Join group: http://www.power.org/apps/org/workgroup/dp/join.php

Group Status:

- Developing new ideas for expanding the charter of the Disruptive Play TWG has been in progress since early April. A diverse set of ideas have been collected and are now being prepared for a presentation to the BoD in June.
- A follow-up VIC update meeting is being planned for 2Q07.

Key Deliverables:

- Charter proposal to be presented at the June BoD.

Key Next Steps:

- Approval of new charter at the June BoD meeting
- Appoint chairperson to lead the group

Embedded Software Framework (a.k.a. Home Media Server) TWG

Objective: The objective of this TWG is to enable embedded software architecture and tools to support Power Architecture multi-core and Power + Accelerators with Home Media Server as an initial targeted collaboration vehicle. The architecture is expected to show the scalability of Power-based solutions across multiple segments such as consumer electronics (as a first target), industrial solution, aerospace and defense, medical, printer and MFP and others.

Chairman: Nobuhiro Asai (IBM) [hms-chair@power.org]

Members: Freescale, IBM

Meetings: TBD

Home page: http://www.power.org/apps/org/workgroup/hms/

Join group: http://www.power.org/apps/org/workgroup/hms/join.php

Group Status:

- Draft level of Home Media Server White Paper was published and released to TC members. This white paper includes a definition of HMS, market opportunities, draft architecture, key technologies, relevant consortiums and future direction.
- Identifying key issues and key influencers (like cable operators, network operators, content owners, etc.) on HMS to refine architecture and design end-to-end solutions.
- Assessing starting an open source project to involve developer level members and community engineers to accelerate this project on both technical and business point of view.
- Actively soliciting more member companies to join.

Key Deliverables:

- Common componentized software framework architecture for various implementations of Power Architecture and some key software components to drive creation of ecosystem.

Issues and Dependencies:

- Additional members are required to cover the wide-range of consumer electronic devices and develop de-facto architecture and core components.
- Discussion with non-member companies (like network operators and content owners)

Key Next Steps:

- Identify key issues on HMS and embedded software starting from consumer electronics segment.
- Draft open source strategy

Software Initiatives TWG

As part of our software initiatives we decided to conduct a one-day Power Architecture Software Summit in Austin, TX, on 19 April. We reached out to Power.org members as well as non-members. We had a total number of 91 registrations and 60 people attending the event. These 60 people participated either in person in Austin at the IBM Customer Briefing center, or joined us remotely via audio and web conferencing. We conducted a survey afterwards in which approximately 30% of the attendees responded.

Overall this was a very successful event, participation was great, feedback was positive and constructive, and participation was lively and active.

Some of the topics presented and discussed were:

- PS3 as developer workstation
- HPC Consortium and Terra Soft Hack-a-Thon
- Power based development workstation discussion
- Business cases around Power Architecture application
- Simulators
- gcc and tool chain presentation
- Transition from 32 bit to 64 bit
- IDEs and Eclipse
- Eclipse-based multicore software development tools
- Power.org website features

The key objectives for this meeting was to educate member companies on what software initiatives are underway in Power.org, to get feedback on those initiatives, and to identify member company software ecosystem problem areas that fit the Power.org collaborative model and would benefit from a directed ecosystem effort. We were also hoping for feedback from non-member companies, specifically from folks that have been around the Linux on Power and embedded Power space for a long time and can share their software experiences (and requirements) with us.

The audience really appreciated the networking opportunity that this event created and is very interested in similar events. We got a lot of detailed feedback and are currently analyzing the information to come up with next steps. There are new software related project possibilities we are discussing, we are considering a follow-up call with the participants to verify that our interpretation of their feedback was correct. We are also considering creating a new working group to focus on possibly designing/architecting a Power-based laptop developer solution.

Additional status summary information can be found below:

Sincerely,

Nina Wilner
Chairman, Software Initiatives TWG

Objective: Help build a well rounded applications portfolio for Power Architecture.
Chairman: Nina Wilner (IBM), [swa-chair@power.org]
Members: IBM, Freescale, Terra Soft Solutions, Virtutech
Meetings: Tuesdays and Fridays, 11:00 AM EST
Home page: http://www.power.org/apps/org/workgroup/swa/
Join group: http://www.power.org/apps/org/workgroup/swa/join.php (or email Nina)
Group Status:

- Collecting results from Power Architecture Software Summit

Key Deliverables:

- List of next step

Random Bytes

Forgot your Power.org username and/or password?

Go to the link below and enter the email address you used to enroll in Power.org: http://www.power.org/kmembership_info/request_password/. A link to create a new password and/or username will be emailed to you.

Unsubscribe from Power.org general email list

To unsubscribe from the members email list and not receive any general announcements, update your Power.org account at http://www.power.org/kmembers/person/change_personal_info and set the "Receive Members Email" option to "No". NOTE: You will continue to receive emails from any committees you have joined.

Acronyms

BoD	Board of Directors
cPAPR	Consumer PAPR
ePAPR	Embedded PAPR
ISA	Instruction set architecture
PAAC	Power Architecture Advisory Council
PAPR	Power Architecture Platform Requirements
SoC	System on Chip
TC	Technical Committee
TSC	Technical Subcommittee – A formal workgroup chartered by the Board of Directors to create specifications and implementations
TWG	Technical Workgroup – An informal workgroup created to explore the formation of a TSC
VIC	Venture capitalist innovation council