

## Technical Committee Newsletter

1Q2007

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### Greetings from the Chair

Team,

I am looking forward to 2007 as our best year since formation for Power.org technical collaboration. We have been working to narrow and refocus our technical initiatives based on the market analysis and member feedback from 2006. We are placing our 2007 Technical Subcommittee emphasis on embedded, consumer, SoC and software. We are discontinuing initiatives in the areas of Storage and Networking. More information on these important areas is provided by the Technical Committee (TC), Technical Subcommittee (TSC), and Technical Work Group (TWG) Chairpersons updates that follow.

We are making great progress towards our **Power Architecture Developer Conference** to be held in Austin, TX, 24-25 September. Please mark you calendars! Putting such a conference together takes a lot of work. I particularly want to thank Kaveh Massoudian and Jesse Stein for their leadership in organizing this “first of its kind” Power Architecture technical event. General information on the conference is available at <http://www.power.org/devcon/07> and the “Call for Papers” is located at <http://www.power.org/devcon/07/callforpapers/>. We’ll need your help and contributions for this conference to be a success. Please take time to review the call for papers and determine what/where you can contribute. Presentations, panels, or education sessions are all opportunities. If you want to assist in the paper reviews or planning, please contact Kaveh [[kaveh@us.ibm.com](mailto:kaveh@us.ibm.com)] or Jesse [[steinj@us.ibm.com](mailto:steinj@us.ibm.com)]. I am certain this will be a great event and I encourage primary representatives to the Power.org Technical Committee to discuss this event within your company and take action to organize your company’s contributions and participation.

Please note the recent release of the 970MP reference design. This has been in development for some time and is the first key deliverable from our Reference Platforms Technical Subcommittee. The design documentation is currently available to member companies on the Power.org website. Access currently requires a corporate member password, but we are planning to make this info available in the public area sometime in the near future.

Two new initiatives are shaping up and have potential to be of high value to member companies. These are: 1) Common Debug Interface (CDI) and 2) Home Media Server (HMS). Both of these efforts are strategic in nature. The CDI will create improvements in ecosystem efficiency and reduced engineering costs through driving standardization in the area of processor debug support. The HMS is predominantly a software initiative to create a Linux-based modular consumer product software framework optimized for Power Architecture processors and Power + Accelerators. A preliminary white paper on the framework concept was authored by Nobuhiro

Asai (IBM) and is presently available to corporate members for review and comment at <http://www.power.org/apps/org/workgroup/hms/download.php/949/latest/%20White%20Paper%20-%20Home%20Media%20Server%20draft%200.9.pdf>. We are currently seeking members to participate in the HMS software framework development. We are in the process of setting up a special conference call on this subject in early March to detail the projected work scope, decide on the approach, and identify resource commitments.

I would like to take this opportunity to welcome Wind River to Power.org. We are glad that they are members and look forward to their leadership and contributions.

Power Architecture ecosystem development is what we are all about. Our goal is to help you lower engineering cost and improve time to market through the development of strategic collaborative architecture specifications, reference designs, and design information of high value and common interest to the members of Power.org.

I look forward to working with each of you in 2007!

Sincerely,

Michael Paczan  
Chairman, Power.org Technical Committee  
[tech-chair@power.org](mailto:tech-chair@power.org)

## Urgent News

- **Marketing opportunities at ESC**

Power.org offers its members many ways to heighten their visibility at the Embedded Systems Conference (ESC), 2-4 April, in San Jose, California. Our sleek, high-impact booth at ESC is optimally placed by the main entrance and will be front-and-center as all attendees walk into the exhibit hall. Power.org itself will be displaying information about its technical and marketing accomplishments, activities and goals with prominent Power.org branding. Power.org members may reserve exhibit space within our booth to highlight their companies' activities and products. For those members with their own booths on the show floor, placing "teasers" in Power.org's space is a great way to direct traffic from our location to yours. Contact [esc@power.org](mailto:esc@power.org) for more information and to become involved.

- **Register for the first Power Architecture Developer Conference**

Be there when we kick off the first ever Power Architecture™ Developer Conference, the only industry event to showcase the full depth and breadth of the amazingly versatile Power Architecture technology. 24-25 September, 2007. <http://www.power.org/devcon/07>

- **Register today for the March Power.org committee face-to-face meetings**

Freescale will host the March face-to-face meetings at Freescale University in Austin, TX. The Marketing and Technical Committees will meet 13 March and the Board of Directors will meet 14 March. The South by Southwest music festival is occurring that same week in Austin. Register and book your hotel room today. <http://www.power.org/apps/mar07mtg/register.php>

- **970MP Reference Design now available**

The Reference Platform Design Technical Subcommittee is pleased to announce the availability of the 970MP Reference Design. This design has been validated using the Open System Stack (which is comprised of Slim Line Open Firmware, XEN Hypervisor, and LINUX operating system). The Reference Platform Design TSC is working diligently to make the Open System Stack for this platform available to the open source community and Power.org members. The Open System Stack will be announced in an upcoming Power.org Newsletter and web posting.

[http://www.power.org/apps/org/workgroup/hvarch/download.php/1085/PPC970MP\\_Ref\\_Schematics\\_Legal\\_021407\\_final.pdf](http://www.power.org/apps/org/workgroup/hvarch/download.php/1085/PPC970MP_Ref_Schematics_Legal_021407_final.pdf)

## Technical Updates

### Technical Committee

Core top-level goals for Power.org in 2007 that are relevant to our technical work are the following:

- Lower the cost for product development (systems, software, tools, and SoCs) with Power Architecture processors relative to the competition.
- Create awareness about the value of Power architecture based products that can contribute to demand generation for members.
- Ensure a viable operational and financial model for Power.org operations.
- Evolve the Power.org business model with a view to the future by creating an “open exchange” optimized for Power architecture offerings and related information and commerce.

We will structure our technical objectives and tactics to support these goals.

The Power.org Technical Committee recently worked to narrow the focus of the Power.org technical initiatives to better align them with active member company interests and resource commitments. In our February monthly TC call, it was proposed and approved that we would discontinue two Technical Subcommittee initiatives. TSCs being discontinued include the Storage Virtual Partition and Packet Switch Interconnects. In both cases, the business strategies for the lead companies changed requiring reconsideration of their value.

The team continues to give attention towards finding ways to better engage the developer community and to pursue specific software initiatives. This has been a tough area for us as we need more software companies in Power.org with worthwhile ideas and a willingness to contribute resources to the software area. Nina Wilner leads this area via a work group. She is working diligently to plan and conduct a 1-2 day software summit meeting (face to face and web-based) in mid-April. Key objectives for this meeting are to educate member companies on what software initiatives are underway in Power.org under existing TSC initiatives, get feedback on those initiatives, and to identify member company software ecosystem problem areas that would fit with the Power.org collaborative model and benefit from a directed ecosystem effort. Please contact Nina Wilner [[new\\_nina@us.ibm.com](mailto:new_nina@us.ibm.com)] for more information on our software and developer initiatives.

Additional summary TC status is provided below.

Sincerely,

Michael Paczan  
Chairman, Power.org Technical Committee

**Objective:** Establish and maintain the overall technical vision and goals for Power.org. Responsible for forming and supervising Technical Subcommittees (TSC) that develop reports, specifications and reference designs.

**Chairman:** Michael Paczan (IBM) [[tech-chair@power.org](mailto:tech-chair@power.org)]

**Members:** AMCC, Cadence, Chartered Semiconductor, Denali, Ericsson, Freescale, IBM, Mentor Graphics, Rapport, Silicon Application, Sony, Synopsys, Thales, Venture Corp, Virtutech, Wistron, Xilinx

**Meetings:** Monthly conference calls. Quarterly face-to-face meetings.

**Home page:** <http://www.power.org/apps/org/workgroup/tech/>

**Join group:** <http://www.power.org/apps/org/workgroup/tech/join.php>

**Status:**

- February call held to plan, organize, and prioritize technical initiatives
- Recent decisions:
  - TC approved Home Media Server TSC Scope Proposal.
  - TC approved Common Debug Interface Charter Proposal.
  - TC approved the phase out of the Packet Switch Interconnects TSC and the Storage Virtual Partitioning TSC due to change in business needs and Power.org focus.

**Events:**

- Technical Committee face-to-face meeting – 13 March, Austin, TX
- HMS approach, work plan, and resource meeting – early March
- Embedded Systems Conference participation – 2-5 April, San Jose, CA
- Software Summit – mid April
- Developer’s Conference – 24-25 September

**Power Architecture Advisory Council**

**Objective:** Discuss ISA contributions to the Power Architecture ISA and vote on whether to make recommendations to IBM regarding such ISA contributions.

**Chairman:** Wolfram Sauer (IBM) [[paac-chair@power.org](mailto:paac-chair@power.org)]

**Members:** Freescale, IBM

**Home page:** <http://www.power.org/apps/org/workgroup/paac/>

**Specs:** **Power ISA Version 2.03** [[http://www.power.org/news/articles/new\\_brand/#isa](http://www.power.org/news/articles/new_brand/#isa)]

**Status:**

- Joe Wetzel is retiring from IBM after 37 years at the end of April. We want to thank him to his many contributions to Power.org and Power Architecture ISA. Wolfram Sauer is taking over the reigns of the PAAC Chair role.
- A Power Architecture summit was held 28-29 November, 2006. Based on the architecture version 2.03 published earlier in 2006 future directions for the ISA were discussed. Topics included:
  - Competitive evaluation of Power ISA
  - Continued architecture convergence
  - Proposals for new instructions and facilities to be included in future ISA versions.
- Work is currently under way to finish Power ISA Version 2.04.

**Key Deliverables:**

- 2Q07 - Power ISA Version 2.04.

**Bus Architecture TSC**

In 2006, the Bus Architecture TSC divided the Power.org Bus Hierarchy into three levels: a low-performance bus level, a mid-performance bus level and a high-performance bus level. Low-performance and mid-performance bus reports were generated giving recommendations at these levels based on bus and IP comparisons that were done.

As we moved to the high-performance bus level, it started to become clear that a bus hierarchy that excluded either the CoreConnect or the AMBA bus families would not satisfy the broad needs of embedded Power Architecture SoCs. One of the challenges for 2007 is to structure the bus hierarchy in a way that includes both families in order to provide SoC designers with the most flexibility when choosing IP for their Power Architecture SoCs. Some redefinition of the low- and mid-performance levels is possible in 2007.

Another big challenge for 2007 is to adopt or specify a high level bus for embedded Power Architecture SoCs that supports multi-core and Power compatible cache coherency. In 2007, it is imperative for the top level bus to support coherent multi-core (or I/O coherency at a minimum).

The TSC believes that any follow-on to the AMBA family would be unlikely to have the cache, synchronization, and atomic operations that are needed or optimized for Power Architecture. I look forward to member companies contributing possible high-performance bus candidates that support coherency and multi-core in a way that is optimized for Power Architecture. If the coherency specified is compatible with existing Power Architecture accelerators such as SPEs, the Core+Accelerator vision would be supported by allowing SPEs to plug directly into the Power.org bus hierarchy.

Additional summary TSC status is provided below.

Sincerely,

Rich Nicholas  
Chairman, Bus Architecture TSC

**Objective:** Define a hierarchy of bus architectures and support structures to standardize the bus interconnect in Power based SoCs, enabling rapid reuse, lower development costs, and increased compatibility.

**Chairman:** Rich Nicholas (IBM) [[barch-chair@power.org](mailto:barch-chair@power.org)]

**Members:** AMCC, Cadence, Denali, Ericsson, Freescale, HCL Technologies, IBM, Mercury Computer, Synopsys

**Meetings:** Tuesdays 4:30-5:30 PM EST

**Home page:** <http://www.power.org/apps/org/workgroup/barch/>

**Join group:** <http://www.power.org/apps/org/workgroup/barch/join.php>

**Status:**

- In the process of revisiting the low-performance and mid-performance levels to incorporate a broader architecture.
- Beginning work on the high-performance bus strategy

**Key Deliverables:**

- Vision and direction white paper Complete
- Low-performance bus report Complete
- Mid-performance bus report Complete
- Mid-performance bus interoperability specification Complete
- Generic bus interface modules to facilitate 3rd party development
- High-performance bus report

**Platform Architecture TSC**

- Objective:** Define and publish base platform arch standards to facilitate development of compliant components and products
- Chairman:** Dave Willoughby (IBM) [[parch-chair@power.org](mailto:parch-chair@power.org)]
- Members:** AMCC, Cadence, Ericsson, Freescale, IBM, IPextreme, Thales, Wistron
- Meetings:** Combined Open Source Firmware TWG and Platform Architecture TSC bi-weekly conference calls, every other Wednesday 11:30-12:30PM EST. Meetings focused on boot architecture and embedded PAPR development. Meetings to discuss server PAPR are scheduled as needed.
- Home page:** <http://www.power.org/apps/org/workgroup/parch/>
- Join group:** <http://www.power.org/apps/org/workgroup/parch/join.php>
- Specs:** PAPR Version 2.0 [<http://www.power.org/members/developers/specs/PAPR>]
- Status:**
- Scope of embedded PAPR version 1.0 defined. Initial outline of ePAPR specification completed. Developing various boot architecture topics, such as device tree characteristics, with Freescale providing technical leadership.
- Key Deliverables:**
- 2Q07 - Embedded PAPR draft version 1.0 approved by TSC
  - 4Q07 - Embedded PAPR approved and published
- Key Next Steps:**
- Continue developing ePAPR specification topics

**Reference Platform Design TSC**

- Objective:** Produce Reference designs that implement HW/FW Standards for various classes of Power Architecture Platforms to lower overall cost of products and offerings for members of Power.org and PowerPC Ecosystem.
- Scope:** Reference designs that incorporate various PPC licensees' micros and will become example implementation of classes of PAPR compliant platforms such as Servers, Workstations, Clients, Laptops, High End Embedded, HPC.
- Chairman:** Kaveh Massoudian (IBM) [[hvarch-chair@power.org](mailto:hvarch-chair@power.org)]
- Members:** Cadence, Freescale, IBM, Mercury Computer, Silicon Application Corporation, Terra Soft, Thales, Wistron
- Meetings:** Meets bi-weekly with the Platform Architecture TSC, every other Wednesday 11-12 EST.
- Home page:** <http://www.power.org/apps/org/workgroup/hvarch/>
- Join group:** <http://www.power.org/apps/org/workgroup/hvarch/join.php>
- Status:**
- Released the first Power.org 970MP based reference design
  - Working on releasing the Open System Stack in support of the 970MP reference design.
- Key Deliverables:**
- Board schematics
  - Board support package made of system firmware and software based on open source initiatives SLOF, XEN, LINUX.
- Key Next Steps:**
- Start work on the refresh of the I/O subsystem for the 970MP reference design to feature PCI-e Gen2.
  - Work with Freescale, AMCC, and P.A. Semi to make available their reference designs.

**System on Chip Design Hierarchy TSC**

**Objective:** Adopt and/or create specifications and standards to enable creation of a world-class SoC design eco-system (tools, methodology, and design enablers) which positions the Power architecture at the industry forefront and offers designers the easiest path to a successful microprocessor based System-on-a-Chip design.

**Chairman:** Magdy Abadir (Freescale) [[soc-chair@power.org](mailto:soc-chair@power.org)]

**Members:** Cadence, Chartered, Ericsson, Freescale, HCL Technologies, IBM, IPextreme, Mentor Graphics, Synopsys

**Meetings:** Monthly on Fridays 3-4PM EST

**Home page:** <http://www.power.org/apps/org/workgroup/soc/>

**Join group:** <http://www.power.org/apps/org/workgroup/soc/join.php>

**Status:**

- Survey "Design Teams" real world lessons learned and collect their input on what would lower the barriers to adoption of Power Architecture cores in SoCs and improve the efficiency of their SoC development around Power Architecture cores.

**Key Deliverables:**

- 1Q07 - Finalize SoC methodology gap analysis report and target those areas of highest priority including verification, ESL, tradeoff analysis tools, and models. Champion Nagu Dhanwada, IBM
- 2Q07 - Publish/adopt guidelines/standards that support SoC best of breed design methodology with Power Architecture (e.g., SPIRIT)

**Issues and Dependencies:**

- Need to recruit additional technical members into this TSC to be able to reach consensus among the proper players in the SoC ecosystem
- SoC methodology is highly dependent on bus architecture decisions (Protocol support, IP availability, verification IP, model availability, etc.)

**Key Next Steps:**

- Recruit additional technical members into the SoC TSC to carry out its mission
- Investigate the issues around the availability of models including functional, cycle-accurate, cycle-approximate models, performance/power models. From an architect point of view as well as from the application SW developer point of view. What is needed, what are the barriers, can we get the vendors, core providers to put together a feasible solution.
- Investigate trade-off analysis tools (including Power) as a key enabler for Power Architecture-based SoCs

## Common Debug Interface TWG

Last fall, AMCC, Freescale and IBM realizing that Power Architecture implementations lack uniform debug interface, environment and methodology, got together to discuss how to tackle these issues. A proposal was then put forward to the TC to establish a Technical Work Group to study if a TSC is needed to produce and maintain a set of specifications that define a common debug interface or a common set of debug interface options for use in Power Architecture implementations. With the approval from the TC the Common Debug Interface TWG was formed. In addition to AMCC, Freescale and IBM, joining the TWG were Ericsson, Mentor Graphics and Virtutech as well as a new Power.org member – Wind River.

The team has identified three major areas that should be standardized and reached the conclusion that a TSC should be formed to address them. The team is finalizing the scope proposal for the TSC with a plan to seek BoD approval by the next Board meeting.

In anticipation of the formation of the TSC, I would like you to consider joining the TSC to help create a more efficient Power Architecture debug environment.

Additional summary TWG status is provided below.

Sincerely,

Chris Ng  
Chairman, Common Debug Interface TWG

**Objective:** Identify areas for a common debug environment, interface and methodology (both software and hardware). Make recommendation to the TC if a TSC should be formed.

**Chairman:** Chris Ng (IBM) [[debug-chair@power.org](mailto:debug-chair@power.org)]

**Members:** AMCC, Ericsson, Freescale, IBM, Mentor Graphics, Virtutech, Wind River

**Meetings:** Thursdays, 2-3PM EST

**Home page:** <http://www.power.org/apps/org/workgroup/dp/>

**Join group:** <http://www.power.org/apps/org/workgroup/dp/join.php>

**Status:**

- Power.org BoD approved charter proposal
- Team is working on the scope proposal. Initial scope defined as:
  - Physical and logical connections
    - Define the physical and link layers for high speed trace interface for high performance processor-based SoCs
    - Define a common physical connector for high speed trace outputs
    - Define a common set of physical connector arrangements on the target
  - Target capabilities
    - Define capabilities to observe the system under test with minimal (or no) intrusion on its normal operation
    - Define a set of functions that the trace interface will implement.
    - Define capabilities for synchronize debug in a multi processor/core environment.
    - Define scaleable debug facilities on the target.
  - API
    - Define a set of API's between debug probe and test and debug software

**Key Deliverables:**

- TSC scope proposal for TC and BoD approval

**Key Next Steps:**

- Complete TSC scope proposal. Seek TC and BoD approval to form TSC

**Disruptive Play TWG**

**Objective:** Provide a collaborative environment in Power.org to develop disruptive plays around Power Architecture.

**Chairman:** Mike Paczan (IBM) interim [[dp-chair@power.org](mailto:dp-chair@power.org)]

**Members:** Freescale, IBM, IPextreme, Mercury Computer, Rapport, Terra Soft

**Meetings:** TBD

**Home page:** <http://www.power.org/apps/org/workgroup/dp/>

**Join group:** <http://www.power.org/apps/org/workgroup/dp/join.php>

**Status:**

- With Juan-antonio Carballo's departure to start his own VC firm, we lost our key leader for this area. The Disruptive Play Work Group has not met since last Fall due to this change. New leadership options are being explored. Additionally, a follow-up VIC update meeting is being planned for 2Q.
- This group will consider meeting at the upcoming TC face to face in March.

**Key Deliverables:**

- New disruptive ideas around Power Architecture (on-going)

**Issues and Dependencies:**

- Initial round of ideas were consumer product platform oriented and current VIC members indicated that they don't invest in consumer product areas. Consequently, we need to expand the VIC membership to include additional areas or alternatively find other avenues for proposal evaluation and consideration.

**Key Next Steps:**

- Identify new chairperson.

**Home Media Server TWG**

**Objective:** Define and publish open architecture and implementation guidelines that cover emerging requirements on consumer electronics to provide Power + Accelerator solutions. Identify open source projects that are relevant to the architecture and guidelines, and ensure combinations and interfaces among open source projects and applications. Power Architecture enablement status will be investigated as well. Prototype key elements of HMS architecture on the reference hardware, and make it available for member companies. (RAND IP policy)

**Chairman:** Nobuhiro Asai (IBM) [[hms-chair@power.org](mailto:hms-chair@power.org)]

**Members:** Freescale, IBM

**Meetings:** TBD

**Home page:** <http://www.power.org/apps/org/workgroup/hms/>

**Join group:** <http://www.power.org/apps/org/workgroup/hms/join.php>

**Status:**

- Home Media Server TSC Scope and Charter documents were published and approved by the TC.
- A preliminary White Paper that includes the definition of HMS, market opportunities, draft architecture, key technologies, relevant consortiums and future direction has been published and is available for corporate member review.
- Actively soliciting more member companies to join.

**Key Deliverables:**

- Home Media Server white paper to be used as a reference for developing Consumer PAPR (cPAPR).

**Issues and Dependencies:**

- Additional members are required to cover the wide-range of consumer electronics devices and develop de-facto architecture and core components.

**Key Next Steps:**

- Meet with Power.org members to explain the architecture for consumer electronics, using HMS as an example. Discuss opportunities in consumer electronics area. Planning a "special" coordination call with presentations and discussion at the upcoming Technical Committee meetings and Power.org Software Summit.

## Open Source Firmware TWG

As we started adapting the existing PAPR specification to include specifications for the embedded space, we concluded it would be valuable to discuss firmware standards/specifications based on open source/existing firmware.

The main goal is to create an embedded PAPR (ePAPR) specification that is a practical specification based on current best practices and developed in consultation with implementers and users, rather than a specification developed in a "vacuum" that later would be handed to implementers and users. This will be a joint effort and we will try to include ongoing efforts in the communities.

Additional status summary information can be found below:

Sincerely,

Nina Wilner  
Chairman, Open Source Firmware TWG

**Objective:** Collaborate on evaluating open source firmware options that will suit all Power.org members.

**Chairman:** Nina Wilner (IBM), [[of-chair@power.org](mailto:of-chair@power.org)]

**Members:** AMCC, Freescale, IBM

**Meetings:** Every other Wednesday 11:30AM-12:30PM EST

**Home page:** <http://www.power.org/apps/org/workgroup/of/>

**Join group:** <http://www.power.org/apps/org/workgroup/of/join.php>

**Status:**

- Outlined and gained workgroup agreement on goals
- Adjusted charter to match new goals and scope
- Began developing first chapter of embedded PAPR (ePAPR) specification
- Agreed on methods of commenting and feedback
- Team discussed and agreed that ePAPR specification would be a practical specification based on current best practices and developed in consultation with implementers and users

**Key Deliverables:**

- Definition of an ePAPR specification
- Documentation of an ePAPR specification
- 2Q07 - Recommendation on sample firmware
- 4Q07 - ePAPR first draft
- 2Q08 - ePAPR final document

**Issues and Dependencies:**

- Trying to get more Power.org members to participate

**Key Next Steps:**

- Examine current embedded firmware open source solutions and their licenses (uboot, SLOF, PIBS, etc)
- Explore ramifications of developing or enhancing a sample firmware implementation under Power.org
- Explore mechanics of how a sample Power.org firmware implementation would be developed, maintained, and distributed

## Software Initiatives TWG

As part of our software initiatives we decided to conduct a one-day Power.org Software Summit in Austin, TX, on 16 April. We will invite Power.org members as well as non-members (including potential new members). The Summit will have a face-to-face meeting component for those willing to travel, as well as an online/webcast/audio portion for remote participation.

The key objectives for this meeting are to educate member companies on what software initiatives are underway in Power.org, to get feedback on those initiatives, and to identify member company software ecosystem problem areas that fit the Power.org collaborative model and would benefit from a directed ecosystem effort. We are also hoping for feedback from non-member companies, specifically from folks that have been around the Linux on Power and embedded Power space for a long time and can share their software experiences (and requirements) with us.

Additional status summary information can be found below:

Sincerely,

Nina Wilner  
Chairman, Software Initiatives TWG

**Objective:** Help build, manage and maintain a well rounded applications portfolio for Power Architecture.

**Chairman:** Nina Wilner (IBM), [[swa-chair@power.org](mailto:swa-chair@power.org)]

**Members:** IBM, Freescale, Terra Soft Solutions, Virtutech

**Meetings:** Tuesdays and Fridays, 11:00 AM EST

**Home page:** <http://www.power.org/apps/org/workgroup/swa/>

**Join group:** <http://www.power.org/apps/org/workgroup/swa/join.php>

**Status:**

- Planning a Power.org software summit. Completed draft agenda and draft audience list. Tentative date set for early April

**Key Deliverables:**

- Complete planning the software summit, including agenda, invite list, and invitations.

**Key Next Steps for Software Summit:**

- Select date
- Book room
- Finalize agenda
- Finalize audience list
- Send invitations out

## Random Bytes

### Forgot your Power.org username and/or password?

Go to the link below and enter the email address you used to enroll in Power.org:  
[http://www.power.org/kmembership\\_info/request\\_password/](http://www.power.org/kmembership_info/request_password/). A link to create a new password and/or username will be emailed to you.

### Unsubscribe from Power.org general email list

To unsubscribe from the members email list and not receive any general announcements, update your Power.org account at [http://www.power.org/kmembers/person/change\\_personal\\_info](http://www.power.org/kmembers/person/change_personal_info) and set the "Receive Members Email" option to "No". **NOTE:** You will continue to receive emails from any committees you have joined.

### Acronyms

|              |  |
|--------------|--|
| <b>BoD</b>   | Board of Directors   |
| <b>cPAPR</b> | Consumer PAPR  |
| <b>ePAPR</b> | Embedded PAPR  |
| <b>ISA</b>   | Instruction set architecture   |
| <b>PAAC</b>  | Power Architecture Advisory Council  |
| <b>PAPR</b>  | Power Architecture Platform Requirements   |
| <b>SoC</b>   | System on Chip   |
| <b>TC</b>    | Technical Committee  |
| <b>TSC</b>   | Technical Subcommittee – A formal workgroup chartered by the Board of Directors to create specifications and implementations |
| <b>TWG</b>   | Technical Workgroup – An informal workgroup created to explore the formation of a TSC  |
| <b>VIC</b>   | Venture capitalist innovation council  |